

CLAIMS

What is claimed is:

1. An error correcting decoder, comprising:
 - (a) a first decoder;
 - 5 (b) a deinterleaver coupled to the output of said first decoder;
 - (c) a second decoder coupled to the output of said deinterleaver;
 - (d) a memory coupled to said first decoder; and
 - (e) a feedback coupled to said second decoder and said memory
and output to said deinterleaver;
 - 10 (f) wherein said feedback errors corrected in a codeword by said
second decoder to correct errors of related codewords in said memory and
thereby correcting errors in said deinterleaver for codewords for said second
decoder.